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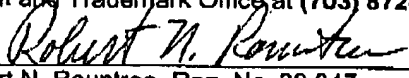
JUN 07 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICEIn re the Application of: **Lee et al.**Docket Number: **TI-36130**Serial No.: **10/689,311**Art Unit: **2816**Filed: **10/20/2003**Examiner: **T.D. Cunningham**

For: **PROGRAMMABLE LINEAR-IN-DB OR LINEAR BIAS CURRENT SOURCE AND
METHODS TO IMPLEMENT CURRENT REDUCTION IN A PA DRIVER WITH
BUILT-IN CURRENT STEERING VGA**

CERTIFICATION OF FACSIMILE TRANSMISSION

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June 7, 2005
Date

FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT (10 Pages)
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT (# Page)
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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Lee et al.	
RECEIPT DATE & SERIAL NO.: 10/689,311	
FILING DATE: October 20, 2003	
TITLE OF INVENTION: PROGRAMMABLE LINEAR-IN-DB OR LINEAR BIAS CURRENT SOURCE AND METHODS TO IMPLEMENT CURRENT REDUCTION IN A PA DRIVER WITH BUILT-IN CURRENT STEERING VGA	
TI FILE NO.: 77-36130	DEPOSIT ACCT. NO.: 20-0668
DATE FAXED: June 7, 2005	
DUE: June 16, 2005	
ATTY/SECY: Robert N. Rountree	

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Lee et al.**

Attorney Docket No.: **TI-36130**

Serial No.: **10/689,311**

Examiner: **T.D. Cunningham**

Filed: **October 20, 2003**

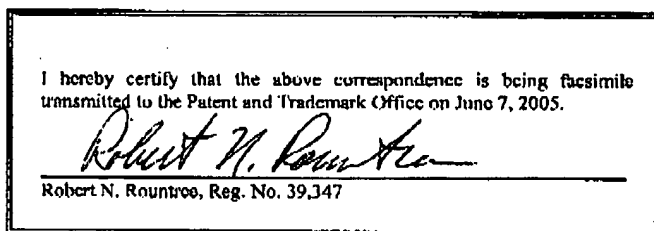
Art Unit: **2816**

For: Programmable Linear-in-dB or Linear Bias Current Source and Methods to Implement Current Reduction in a PA Driver with Built-In Current Steering VGA

AMENDMENT UNDER 37 C.F.R. § 1.112

June 7, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



Dear Sir:

Reconsideration is requested in response to Examiner's rejection dated March 16, 2005. Please amend the instant application as follows.